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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): SATO et al

Serial No.: Rule 1.53(b) continuation of U.S. Patent Application Serial
No. 09/709,403 filed November 13, 2000

Filed: Herewith

For: METHOD OF MANUFACTURING A SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE AND A SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

Group of Parent: 2813

Examiner of Parent: W. Vesperman

PRELIMINARY AMENDMENT

Commissioner of Patents
P. O. Box 1450
Alexandria, VA 22313-1450

November 26, 2003

Sir:

Please amend the above-identified application, prior to examination thereof, as follows:

IN THE SPECIFICATION

Please amend the specification as follows:

Page 1, before line 5 "FIELD OF THE INVENTION", insert the following:

--CROSS REFERENCE TO RELATED APPLICATIONS